Learning Maps Between Sensorimotor Systems on a Humanoid Robot

by

Matthew J. Marjanović

S.B., Massachusetts Institute of Technology (1993)

Submitted to the Department of Electrical Engineering and Computer Science
in partial fulfillment of the requirements for the degree of

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Ack.

This is not my Life's Work.

But, if it's all the same to you, I'd like to thank those people who made this possible, or at least plausible.

First and foremost is **Matt Williamson**, because the mystic visions behind this project were the outcome of a particularly inspired conversation we had, months ago. Second and foremost is, of course, **Rod Brooks**. Rod has made Cog possible, has made my graduate career plausible, and has signed this document.

Thanks to those Usual Suspects: **Cindy** and **Yoky**, officemates par excellent, who made the PleasureDome possible (and that is only the beginning); **Scaz** and **Robert**, who have shared these last few late nights with me, albeit in disparate parts of the building.

Mike Bolotski is still a nice guy. He would be even if he didn't drive me to hockey games.

Maja and **Rich**, the happy couple, deserve mention. Rich is a dude; Maja incessently offered to help with proofing this thesis. Maja also got me involved in all this; send all complaints to her.

(Dear Reader: Listen to your Jesus Jones CD's again. Nothing has changed.)

I'd like to thank **Jose**. You gotta give that guy some credit, so I just did.

My apologies to **Liana**; this thesis is not a choose-your-own-adventure novel. I'll save that spark for later, I suppose.

Mike Binnard — he built Cog, you know. Solid, bulletproof. Too bad he and **Gina** have swept each other away to the Sunshine.

Eric Jordan, my dear old friend Eric Jordan, deserves a bullet in the head for no other reason than his constant comment of "Give it up." Of course, he did it for me; look, I finished, didn't I? Lucky I didn't punch him out is all I can say. Thanks. I do appreciate it.

Roommates **Diane** and **Sinan**, on the other hand, gave me the more traditional words of encouragement. Will the cleaning ever end?!?

Farewell to **Lisa T**, although she's not going so far. The connection will always be there.

Greetings to **Evan R**. He is a friend, despite his worries to the contrary.

Hi-5 to **Dave B**. The week of Sunshine he gave to the Matts did wonders for my complexion.

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Did I mention that all the cheesecakes were very delicious? So was the jello. Green and red.

Finally, of course, **Mom** and **Tash**, because I wouldn't be me without the two of them.

Welcome to *ThesisLite*. Sit back, pop the top, and enjoy.

—Matto

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Chapter 1

Background

This thesis describes some preliminary work I have done with the visuomotor system of Cog, a robot currently under development at the MIT Artificial Intelligence Laboratory. Cog is a humanoid robot, designed to be able to interact with the world in a human-like fashion, hopefully with human-like capacities. The purpose of the Cog project is to learn about human cognition by attempting to simulate or replicate it, with the underlying hypothesis that cognition is shaped by interaction with the world.

As one of the first toddling steps towards that goal, I implemented a system which learns a model of head movement in terms of the visual motion it induces by moving the camera. This is one stab at what I hope will eventually be many such models linking many (all) of Cog's sensorimotor systems together. I have implemented a single form of such a model, based on a network of linear approximators; many alternatives exist. In the future, I would like to have a system which automatically generates these functional maps between systems, picking the appropriate format or using a sufficiently general and powerful template.

This idea of a phalanx of internal models is inspired by the cerebellum. Another higher level goal of mine is to implement cerebellar functions on Cog; a large part of that task involves figuring out just what the cerebellum does. One current view is that the cerebellum makes dynamic models of interactions of the body with the world, as well as models of internal systems and their interactions. Along these lines, generating models of sensorimotor systems is an implementation of cerebellar function.

From a more pragmatic perspective, as Cog becomes more developed, it will have more and more specialized computational systems which will need to cooperate with each other. Models which translate the actions of one system into the native parameters of another will facilitate such cooperation. Once the number of subsystems on Cog exceeds a handful, an infrastructure which automatically generates such adaptive models will make the entire programming experience much more manageable.

Cerebellum

The primary inspiration for this project comes from the cerebellum, an organ which is well-known but little-understood. In humans, the cerebellum is a corrugated golf ball of neurons tucked into the back of the skull between the occipital lobe and the brainstem (Figure 1-1). Its cortex is about 1mm thick, but deeply folded, and comprises about half the neurons in the brain. Unlike the cerebral cortex, it has no central fissure. The microscopic structure of the cerebellar cortex is crystalline compared to the cerebral cortex; of the five major cell types, four are oriented and located in well-defined perpendicular planes. The cerebella of other species have roughly the same structure; all vertebrates have a cerebellum. A full discussion of the intricacies of cerebellar physiology can be found in (Ito 1984), or (Eccles et al. 1967).

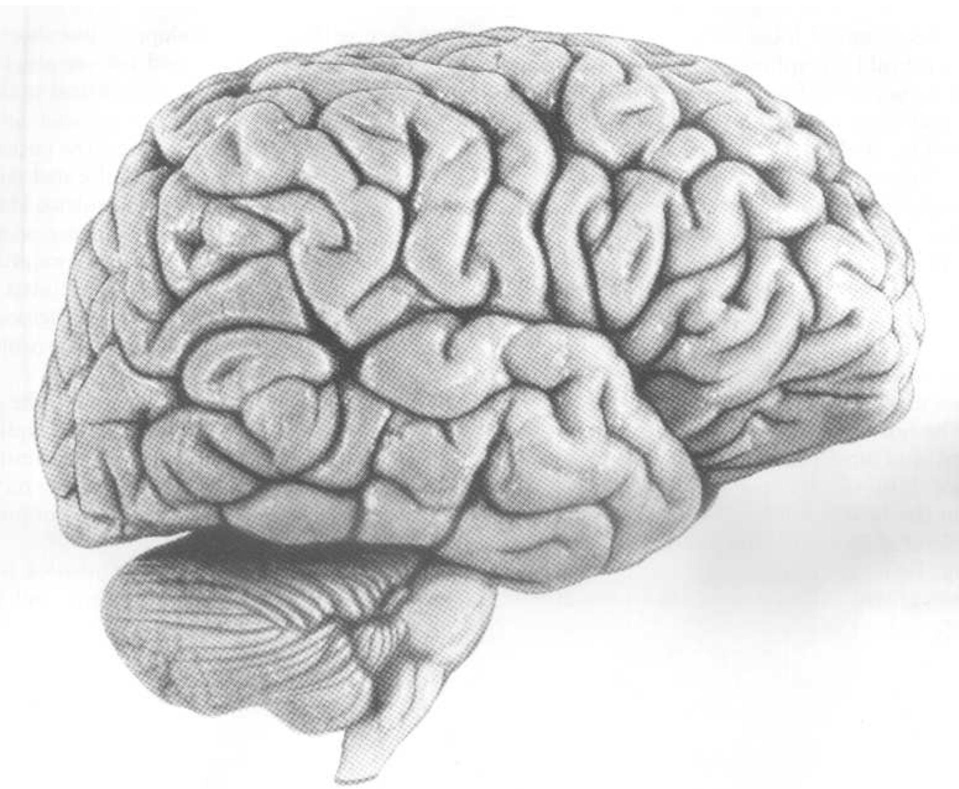

Figure 1-1: Sagittal view of the brain. The cerebellum is the finely folded structure in the lower left, tucked under the cerebrum and behind the brainstem (Noback 1981, p.3)

The first major step in unraveling the functioning of the cerebellum was made by Holmes (1917), a military doctor who carefully documented the motor deficits of WWI soldiers suffering from head wounds. This became the first volume of a great body of evidence pointing towards the human cerebellum's involvement in motor control.

So far, via such lesion studies, we know that the cerebellum is involved in both autonomic and voluntary motor activity. The medial cerebellum controls reflexes such as the vestibular ocular response (VOR). The paramedial cerebellum helps with posture and stance. The lateral cerebellum plays a part in coordinating voluntary motor activity. Lesions of the lateral cerebellum cause well-known motor deficits (Dichgans & Diener 1985):

- diminished tendon reflexes (*hypotonia*)
- weak, easily tired muscles (*asthenia*)
- decomposition of movements into component parts
- inability to correctly gauge distance of movements (*dysmetria*)
- inability to perform smooth alternating, repetitive movements (*dysdiadochokinesia*)
- oscillation in voluntary motion (*intention tremor*)
- drunken-like walking (*ataxic gait*)
- slow, explosive, slurred speech (*scanning speech*).

Note that in no case does cerebellar damage prevent voluntary movement; it just degrades the quality.

The outcome of such lesion data was the general hypothesis that the cerebellum's function is to learn motor programs for common, repeated motions, such as reaching out to different points in space. These programs would be used by the cerebral motor cortex to create smooth, well-controlled voluntary motions. Deficits such as intention tremor become a result of the cerebral cortex being forced to use its own slow feedback loops to control activity which should have been directed by a feedforward motor program. Marr (1969) and Albus (1971) introduced the first of the modern theories that try to explain how the cerebellum accomplishes this learning and control. Both theories mapped the cerebellar microstructure into a three-layer network of perceptrons which acted as an associative memory system.

Modelling

In the last twenty-five years, the field has evolved to a more elegant hypothesis, that the cerebellum's function is to create and learn dynamic models of the body and the body's interaction with the world. These models are not restricted to the motor systems, but encompass sensory systems as well, and may even include models of cognitive subsystems within the brain.

Paulin (1993*b*) cites examples of vertebrates which have large and highly developed cerebella, but primitive motor skills. The Mormyrid fish, one such example, are electric fish which use electrolocation to navigate and feed, and the enlarged portions of their cerebella are tied to electrodetection. In mammals which rely on echolocation, such as bats and dolphins, certain lobules of the cerebellum which react to input from the auditory system are very highly-developed, compared to lobules related to wings or fins. In all these cases the cerebellum plays a greater role in processing sensory information than in regulating motor output.

Daum et al. (1993) cite recent reports in which cerebellar lesions in people result in:

- impairments in visuospatial recall and in three dimensional manipulation
- deficits in anticipatory planning
- deficits in verbal learning
- changes in visuospatial organizational abilities.

Furthermore, a study by Akshoomoff & Courchesne (1992) gives evidence that the cerebellum plays a role in quickly shifting attention between different sensory modalities.

All of these are hard to explain if the cerebellum's function is restricted to motor programming; however, these and motor programming can all be explained by viewing the cerebellum as a dynamic systems modeller. Historically, it may just be that the motor deficits were the most obvious results of cerebellar dysfunction in humans.

Modelling the Modeller

This brings us back around to my own project. Many theories have been advanced to try to explain how the cerebellum can learn and maintain such models; no one has yet answered that question. Paulin thinks that the cerebellum acts as a Kalman filter (Paulin 1993*a*). Miall, Weir et al. (1993) advocate the idea that the cerebellum is a Smith predictor, a system which models time delays in the control loop as well as kinematics. Keeler (1990) returns to the Marr-Albus models, but recasts them as prediction generators rather than motor program memories. Kawato et al. (1987) model the cerebellum as networks of non-linear elements which learn both forward and inverse dynamics from each other.

All of this has inspired the simple system implemented on Cog, which learns a kinematic model of the head in terms of vision. The learning is performed by a network of linear experts (Section 3.2.2). This system is the first solid step towards an implementation of cerebellar functions on Cog.

Chapter 2

Apparatus

This project was implemented on the robot Cog; one purpose of the project was to see just what Cog was capable of at this early stage in its electromechanical development. This chapter gives an overview of Cog's mechanical and electrical features, followed by a more detailed look at the framegrabbers, motor control software, and IPSmacrolanguage. These are hardware and software systems which I designed myself and which are a integral parts of this project.

2.1 Cog in General

Cog is a robotic humanoid torso (Figure 2-1). Mechanically, Cog is an approximation of the human frame from the waist up, with all the major degrees of freedom [dof] accounted for, except for the flexible spine. Electronically, Cog is a heterogeneous, open architecture, multi-processor computer.

Body

Cog's body consists of a torso, a head, and two arms. The torso is mounted to a large, sturdy machine base via 2-dof "hips"; Cog has no legs. The head is mounted on a 3-dof neck, and the neck and arms are connected to a 1-dof shoulder assembly which rotates about the vertical axis of Cog's torso. Cog's arms are 6-dof mechanisms with compliant (ie. spring-coupled) actuators (Williamson 1995). Hands for Cog have also been designed and are currently being tested (Matsuoka 1995). (Figure 2-2, A, B)

All of Cog's actuators use electric motors with optical encoders to measure joint position. The motors are controlled by dedicated 68HC11 microprocessors, one processor per motor. The 68HC11's regulate motor power via a pulse-width modulation (PWM) circuit, and can measure motor current, temperature, and position.

Cog's head holds two 2-dof eyes, each composed of two miniature black and white CCD cameras. One camera has a wide-angle lens with a 115° field of view to cover full-field vision. The other camera has a 23° narrow-angle lens to approximate foveal vision. The cameras interface to Cog's brain via custom-built frame-grabbers (see Section 2.2). At this moment, the cameras are the only sensors on Cog's head, however Cog will soon have hearing (Irie 1995) and a vestibular apparatus as well.

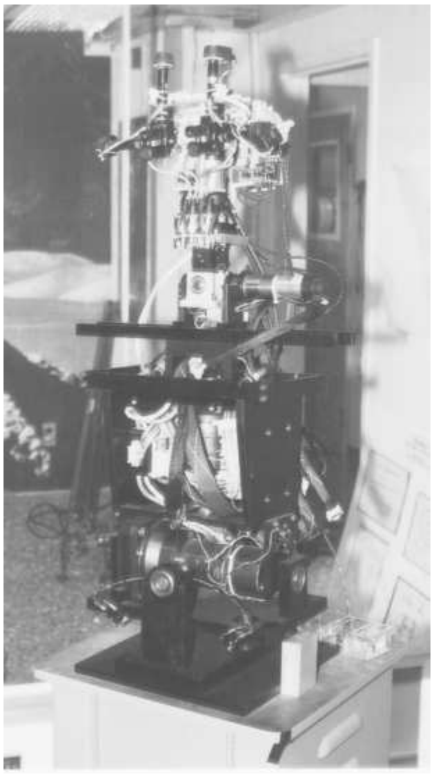

Figure 2-1: Front view of Cog, a humanoid robot from the waist up. The arms have been taken off for table-top testing.

Cog will eventually also have tactile sensors around its body.

Brain

The architecture of Cog's brain is based on a backplane containing multiple independent processor boards (Kapogiannis 1994). Processors can be sparsely connected one-to-another via dual-ported RAM (DPRAM). The backplane supplies the processors with power and a common connection to a front-end processor (FEP), which provides file service and tty access. In the current scheme, up to 16 backplanes can each support 16 processors.

We are currently using Motorola 68332 microcontrollers as the main processors. These are 16 MHz variants of the 68020, with a 16-bit data bus and on-chip serial I/O hardware. Each processor is outfitted with 2 Mb of RAM and each runs its own image of L, a multitasking dialect of Lisp written by Rod Brooks (1994). The processors have six DPRAM ports which can be used for communication with other processors, or for interfacing to framegrabbers and video display boards. They also have a 2 Mbit/s synchronous serial port through which they connect to the peripheral motor control boards.

The brain is described as an open architecture system because it could conceivably

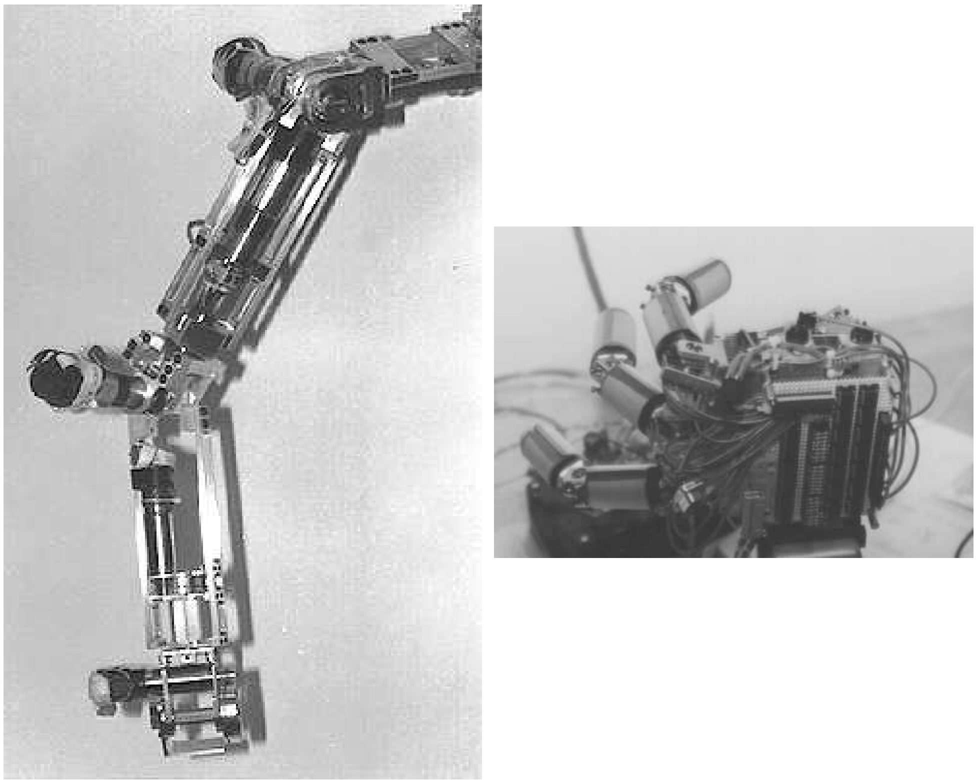

Figure 2-2: One of Cog's arms and one of Cog's hands, currently disembodied.

contain any processor which uses the same FEP and DPRAM interfaces. A C-40 based DSP board with DPRAM interface has been built and will soon be integrated into Cog for use with audition and vision.

2.2 Frame Grabbers

Cog's vision system consists of four black and white commercial CCD cameras and four custom-built framegrabbers (Figure 2-3), which were designed and built in-house by me. The primary motivation for designing our own was the issue of interfacing to Cog's brain. Any over-the-counter framegrabber would have needed an add-on DPRAM interface. Monochrome framegrabbers are not very complex; it was cheaper and easier to build our own grabbers which stored images directly into DPRAM's.

The framegrabbers operate in real-time, dumping one 128×128 pixel frame to DPRAM every $^1/_{30}$ second. Each pixel is 8-bit grayscale; 128×128 is the largest image array that will fit into a DPRAM. After a frame is complete, the DPRAM generates an interrupt request on the 68332 processor to indicate that the frame is ready for processing. Each framegrabber has slots for six DPRAM cards, so that six processors can simultaneously and independently access the same camera image. Each grabber also has a video output for debugging which can display the raw digitized image or

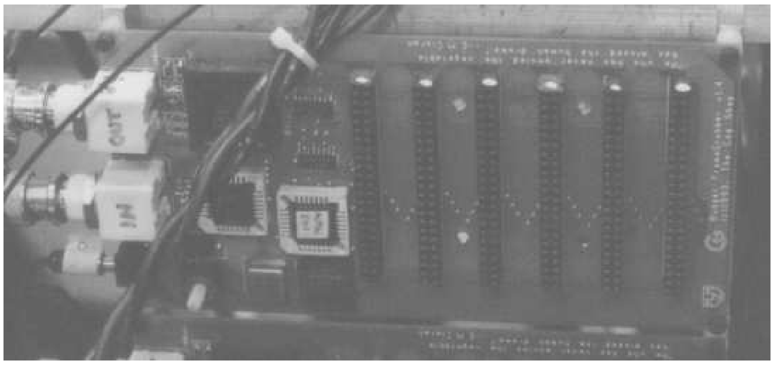

Figure 2-3: One of Cog's home-built real-time framegrabbers. The circuit board is dominated by the six ports for DPRAM cards.

the contents of a DPRAM.

The basic outline of a framegrabber is shown in Figure 2-4 (the complete schematic can be found in Appendix A). This circuit is a conglomeration of single-chip solutions (which explains why it is relatively cheap and easy to build). A sync separator chip extracts timing signals from the video input. A Brooktree Bt218 analog-to-digital converter (Bro 1993) digitizes the video input and puts the pixel data onto a bus. Two counter chips count pixel column and row, and control addressing of the DPRAM's. A Brooktree Bt121 videoDAC generates a video output of whatever is on the data bus for debugging. All of the support logic is contained in two programmable array logic (PAL) chips. The Decode PAL controls read/write to the data bus and handles the debugging switches. The Clock PAL generates the pixel clock, various blanking pulses, and the write pulses for the DPRAM's. A 19.66 MHz crystal oscillator drives the Clock PAL.

The 19.66 MHz oscillator is divided down to 2.46 MHz for the pixel clock by the Clock PAL — this yields a 128-pixel horizontal line. The video signal has 525 lines vertically; we get 128-pixel vertical resolution by digitizing only every other line of every other field. Each video frame is transmitted as two interlaced fields (Roberts 1985), one of which is completely ignored by the framegrabber.

Each of the video fields is $^1/_{60}$ of a second long. The grabber writes to a DPRAM during the even field, but ignores the DPRAM during the odd field. This gives a processor 17 ms to respond to the image-ready interrupt and to use the data before it gets clobbered. With the current system software, the frame is immediately copied from DPRAM into local memory. Thus, there is still a complete 33 ms latency (one video frame) between when the image is captured and when the brain sees it.

Note that the circuit does not contain a phase-locked loop (PLL). The sytem clock (the oscillator) is not directly synchronized with the video input signal. However, the pixel clock divider is reset with every horizontal line. So, there is at most a $^1/_8$ pixel jitter between lines.

The framegrabbers have gone through a few revisions, mostly via PAL firmware

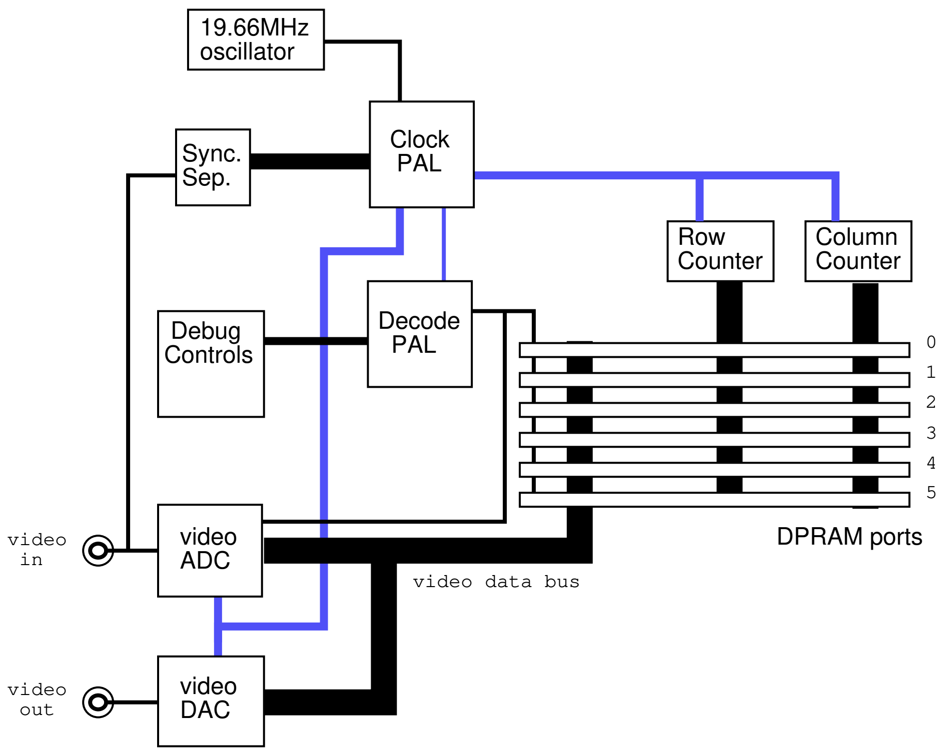

Figure 2-4: Functional diagram of a framegrabber.

upgrades. Since almost all of the control logic is routed through the PAL's, overhauls of the system are much easier and cheaper than if the logic had been hardwired. Framegrabbers v1.4.3 were used for this project; a new set of PC boards with extra circuitry for improved timing and image stability (v.1.5) are almost ready to be put into service.

2.3 InterProcess Socks

L is a multi-tasking dialect of Lisp, which allows multiple procedures to execute with simulated concurrency. However, L has very little in the way of built-in process control, and no provisions for interprocess communication. To fill these gaps, I wrote IPS.

IPS, or InterProcess Socks, is a macro package which provides for simple but very convenient data sharing and process control via two abstractions, the `svariable` and the `sprocess`.

A single `svariable` (for *shared*-variable) consists of two parts, a datum and a flag. A lone `svariable` is no better than a regular variable; the fun starts when many svariables are `connected` together to form a connection group. Connected svariables share the same data, via an indirected pointer, but have independent flags.

When the common data is changed by access through one svariable, the flags on all connected svariables are set. Svariables can also be `disconnect`ed, or separated from their groups.

A `sprocess` (for *sharing*-process) is a kind of execution-independent process object with input and output ports. A sprocess contains executable code which can be spawned to create a real, running process; this process can be killed and respawned without obliterating the sprocess. A sprocess also has *port variables*, variables which are lexically bound within the sprocess body but can be accessed outside of the sprocess. Port variables can contain any data structure, but by default they are svariables, which means that the ports of different sprocesses can be connected together. The body of a sprocess is specially parsed so that the data fork of a shared port variable can be accessed by name like any other variable.

Shared port variables are the cornerstone of IPS. They allow the programmer to create individual lexically-scoped processes and to easily specify data connections between them. All of the code for this thesis is written using IPS. Each subsystem on Cog is composed of individual sprocesses —motor handlers, video frame handlers, calibration processes, etc.— which are linked together by connected svariables.

A full description of IPS can be found in Chapter 2 of the *HumOS Reference Manual* (Brooks, Marjanović, Wessler et al. 1994); a quick overview of the command syntax is given in Appendix B.

2.4 Motor Control

Each of Cog's motors is driven by a dedicated peripheral controller board. These boards contain a 68HC11 8-bit microprocessor, a pulse-width modulation (PWM) circuit, a high power H-bridge for motor current control, amplifiers for current and temperature sensors, and an encoder chip to keep track of joint position. Each microcontroller communicates with a 68332 processor via a synchronous serial line by exchanging 16-byte packets containing 13 bytes of data, tag bits, and a checksum. A single 68332 can service eight controller boards with a total throughput of up to 400 packets per second. In the case of the neck, with three motors and controllers hooked up to one processor, the data rate is a comfortable 100 Hz apiece.

The synchronous serial protocol specifies that the 68332 is the master port, initiating byte transfers between it and a 68HC11. In general, communication from the 68332 side is driven by a motor handler coded as an sprocess using IPS. The handler has svariables for each of the command parameters and regularly stuffs the data into the appropriate packets and ships them off. Return packets are parsed and the sensor data is placed into other svariables. Any other sprocesses which want to use this data, or want to command the motor, simply need connections to the right ports in the motor handler.

The motor controllers havea couple of built-in safety features. If communication with the master 68332 processor is lost for more than 250 ms, or if the 68HC11 hangs, it will automatically reset itself and shut down its motor. Furthermore, whenever a controller is restarted, it waits to handshake with the master processor before starting

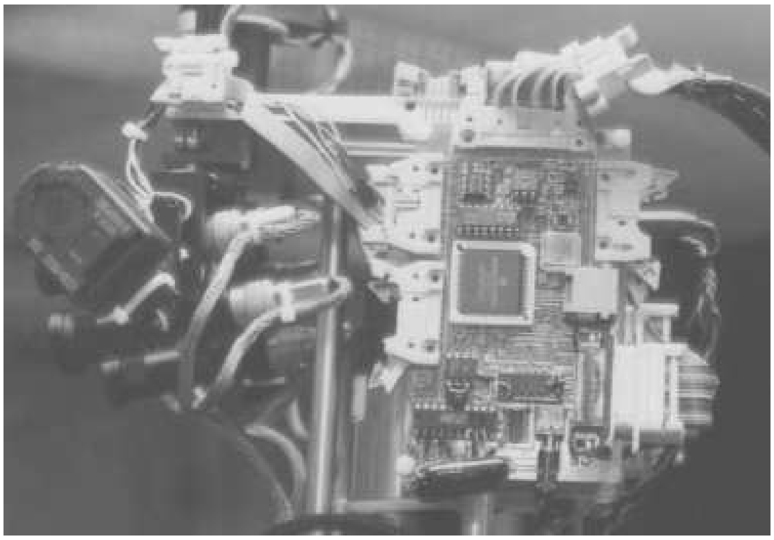

Figure 2-5: A 68HC11 peripheral motor controller board, in this case mounted on the head to control an eye motor. The various ribbon cables are for power, sensor and encoder input, motor output, and communication with a master 68332 processor.

its main program. This *startup-interlock* involves the exchange of three query packets and three acknowledgement packets to make sure that the computers on both ends of the serial link are ready and willing to start processing. During the interlock, the controller also identifies what type and version of code it is running, and gives a guess as to why it was last reset.

The bulk of the code on a motor controller is for servocontrol. The servo code decides just what kind of muscle a motor looks like to the rest of Cog's brain. After reviewing some literature on theories of biological motor control (Bizzi, Hogan et al. 1992), and equilibrium point control (McIntyre & Bizzi 1993) in particular, I decided to implement a basic proportional-derivative (PD) controller for the neck motors. The PD controller has adjustable gains so it is possible to execute equillibrium point guided movements from the 68332, and as a fallback, it always has plain position control.

Other actuators on Cog use different servocontrol code. The eye controllers have proportional-integral-derivative (PID) controllers optimized to perform unassisted position-directed saccades. The arm controllers have torque feedback, and take PD control to the point of simulating virtual springs in the joints.

Chapter 3

Task

The task at the root of this project is to accurately determine motion in the visual scene in spite of any movement of the head. The basic set-up is illustrated in Figure 3-1. The head has two degrees of freedom, pan θ_p and tilt θ_t.[1] When the head moves, the visual field of the camera is swept across the scene. If Cog is looking for motion in the scene, it will mistakenly determine that the whole world has moved, unless the motion detection processes receive notice of head movement and can compensate accordingly.

This task has three components:

- detecting motion
- moving the head
- learning the transfer function between head motion and scene motion.

The transfer function depends on the head and camera geometries. This function could be hard-coded, but learning it yields a more robust system. Continuous, slow learning allows this system to adapt to inevitable changes such as motor shaft slippage and mechanical disalignment. Learning can also be easier: why waste time measuring parameters which are due to change if the system can figure out the parameters for itself?

The solution to this task is embodied in the control loop illustrated in Figure 3-2. A motion detection system outputs vectors which show motion velocity in the scene. At the same time, the head moves because the neck motors are commanded by some motion generator. Neck positions and differential movement are fed into a network which produces a velocity correction for the motion detector. Assuming that most of a visual scene is still and that most movement is not correlated to head motion, the corrected detector output should be zero across the scene. So, the corrected output itself can be used as the error signal to train the network.

Any real movement in the scene will generate noise in the network training input. If the learning rate is low, this noise will not significantly disturb the network. The

[1] Cog's neck has a third degree of freedom, roll, which is held fixed in this project, along with the eyes. Integrating three more degrees of freedom is another project altogether (See Chapter 5).

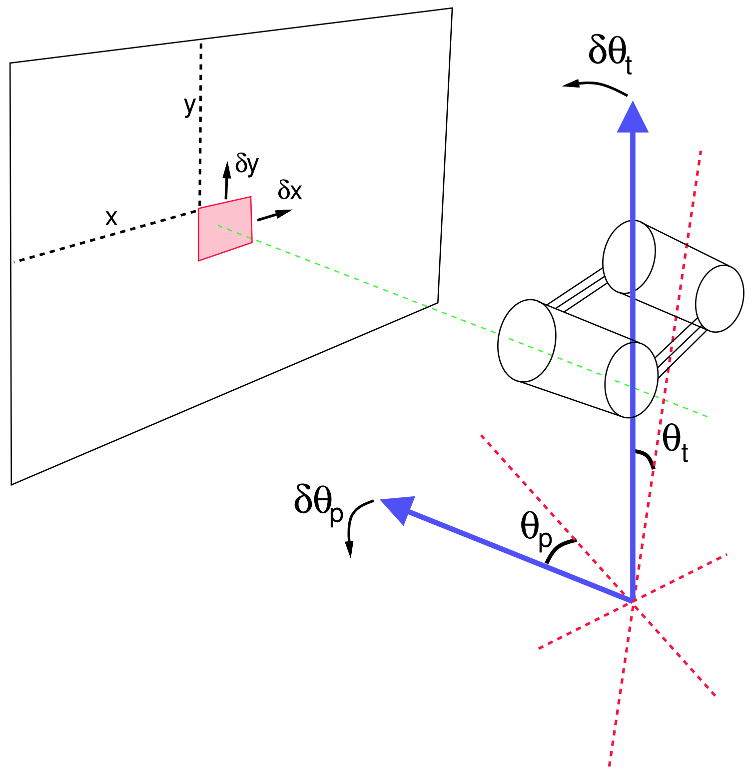

Figure 3-1: Parameterization of the motion detection task. (x, y) is a coordinate in the visual field; (θ_p, θ_t) represents the orientation of the head. When the head turns by $(\delta\theta_p, \delta\theta_t)$, the shaded patch appears to move by $(\delta x, \delta y)$.

entire system will give correct velocity outputs for real motion in the scene, while continuously learning to mask out false motion induced by a moving head.

3.1 Sensorimotor Subsystems

My goal is to learn maps between sensorimotor systems, and I chose neck motor control and motion detection as the first pair because they were available. When I started on this project (and actually continuing to now), the only reliably functioning parts on Cog were the neck motors, the eye motors, and the cameras and frame grabbers. The eye motors and their servo code were the subject of some experimentation by a fellow graduate student (Cynthia Ferrell), so I effectively had only the neck and cameras to work with — one sensor system and one actuator system.

What I came up with is a simple but extensible motion detection system, and a rather degenerate neck motion generator. The motion detection system will probably be a permanent feature on Cog, in a more refined form. The motion generator will hopefully be unnecessary later on.

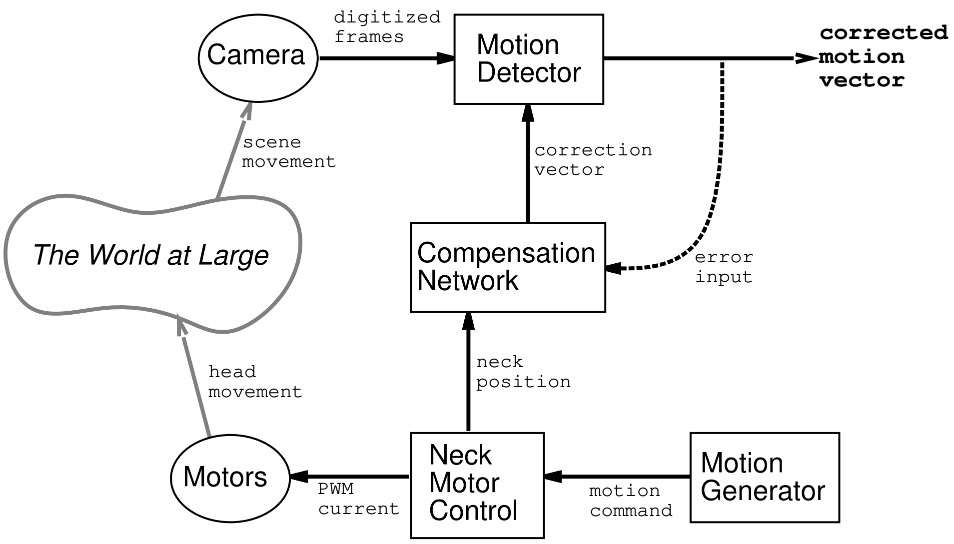

Figure 3-2: The basic control and training loops of the motion detection task. The compensation network learns the effect of head movement on scene movement in order to cancel it out.

3.1.1 Motion Detection

The motion detection system consists of a number of independent detector processes, each of which analyzes the motion of a small patch of the visual field. The output of each detector is a motion vector indicating the velocity of the patch. Taken together, the resulting vector field yields a coarse approximation of the optical flow in the scene (Horn 1986, p. 278ff). Due to processing limitations, only a single detector process was ever used at once, though.

A detector operates on a 16×16–pixel patch located at some position (x, y) in the visual field (recall that the full field is 128×128). The detector operates as follows (refer to Figure 3-3):

1. It grabs and stores a 24×24 patch centered over the 16×16 patch at (x, y).

2. From the next successive video frame, it grabs the 16×16 patch.

3. It searches for the best correlation of the 16×16 patch within the 24×24 patch. The offset of the best match from the center yields the motion vector.

The best correlation is found by calculating the Hamming distance between the 16×16 patch and its overlap at each of 81 positions within the 24×24 patch. The position with the lowest distance wins.

To make even a single detector operate in real-time on a 68332 processor, each 8-bit grayscale pixel in a patch is reduced to 1-bit by thresholding with the average