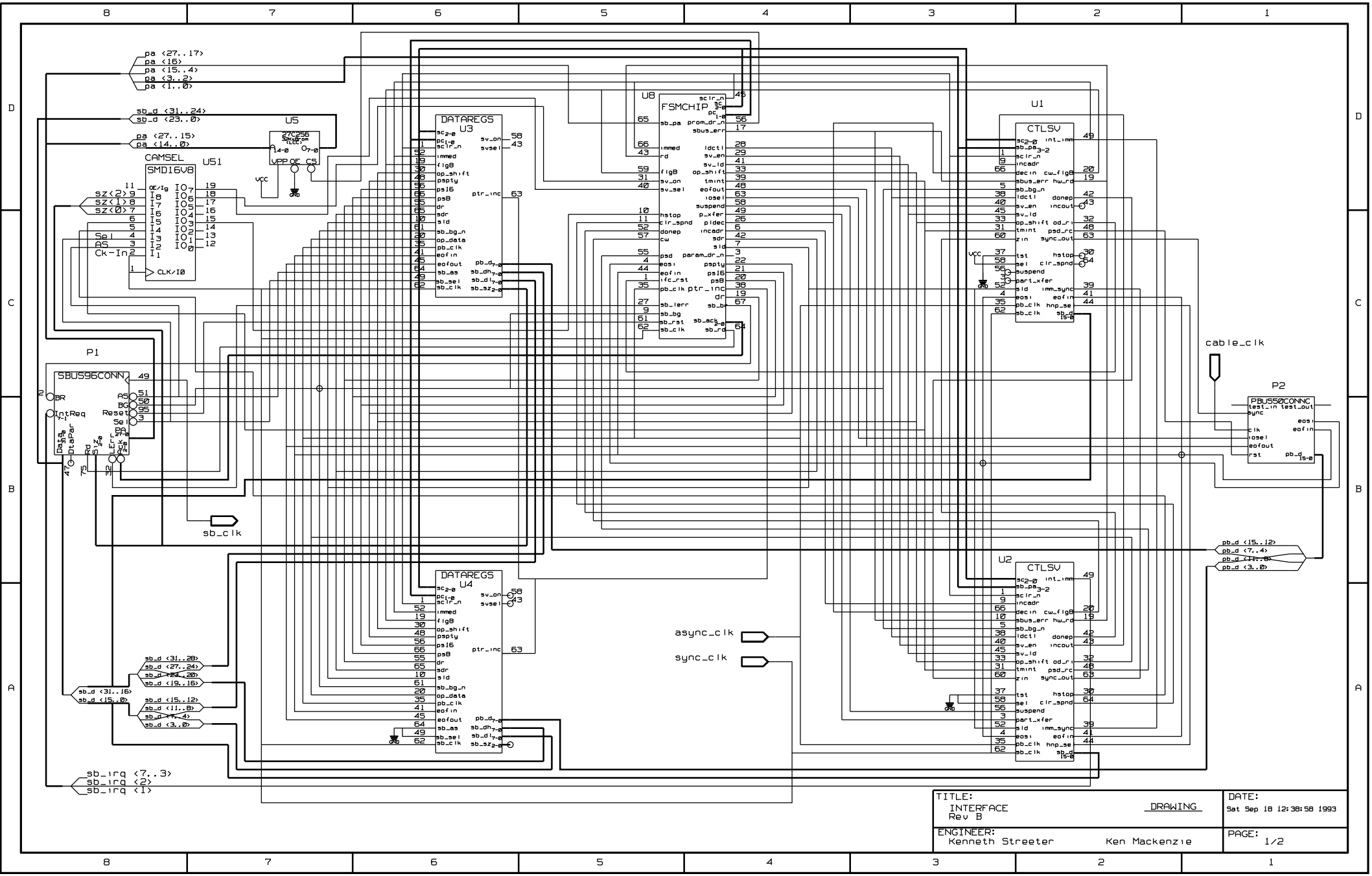
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sb_d <23..0>
pa <27..15>
pa <14..0>
U5
27C256
EPROM
(LCC)
A14-0
O7-0
VPP OE CS
VCC
GND
CAMSEL
U51
SMD16V8
Sel
AS
Ck-In
SZ<2>
SZ<1>
SZ<0>
CLK/I0
P1
SBUS96CONN
BR
IntReq
Reset
Sel
AS
BG
DATAREGS
U3
DATAREGS
U4
U8
FSMCHIP
U1
CTLSV
U2
CTLSV
cable_clk
P2
PBUS50CONNC
sb_clk
async_clk
sync_clk
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TITLE:
INTERFACE
Rev B
DRAWING
DATE:
Sat Sep 18 12:38:58 1993
ENGINEER:
Kenneth Streeter
Ken Mackenzie
PAGE:
1/2