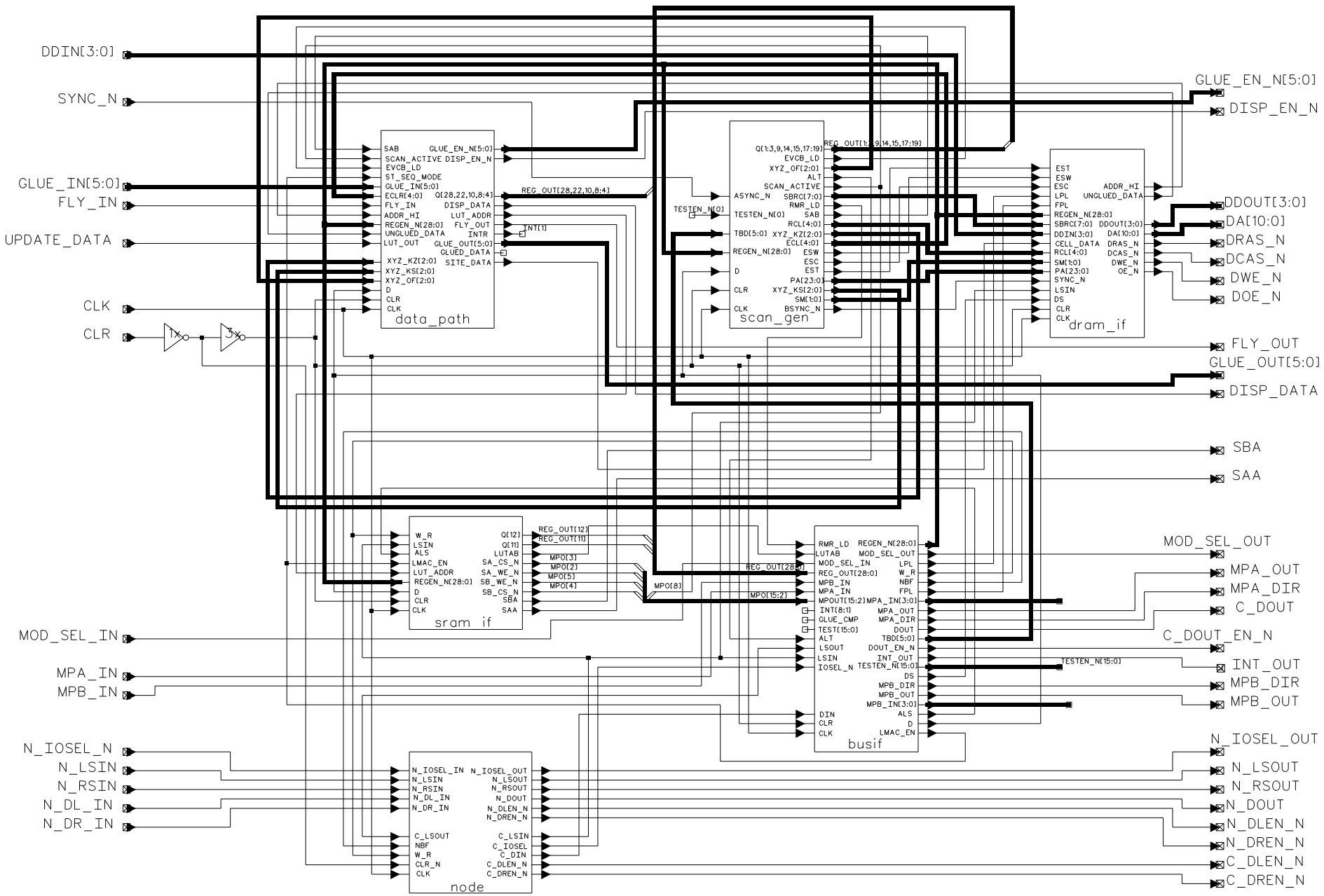
DDIN[3:0]
SYNC_N
GLUE_IN[5:0]
FLY_IN
UPDATE_DATA
CLK
CLR
MOD_SEL_IN
MPA_IN
MPB_IN
N_IOSEL_N
N_LSIN
N_RSIN
N_DL_IN
N_DR_IN
GLUE_EN_N[5:0]
DISP_EN_N
DDOUT[3:0]
DA[10:0]
DRAS_N
DCAS_N
DWE_N
DOE_N
FLY_OUT
GLUE_OUT[5:0]
DISP_DATA
SBA
SAA
MOD_SEL_OUT
MPA_OUT
MPA_DIR
C_DOUT
C_DOUT_EN_N
INT_OUT
MPB_DIR
MPB_OUT
N_IOSEL_OUT
N_LSOUT
N_RSOUT
N_DOUT
N_DLEN_N
N_DREN_N
C_DLEN_N
C_DREN_N
data_path
scan_gen
dram_if
sram_if
busif
node
REG_OUT[28,22,10,8:4]
REG_OUT[12]
REG_OUT[11]
REG_OUT[28:0]
MPO[8]
MPO[15:2]
TESTEN_N[0]
TESTEN_N[15:0]
INT[1]