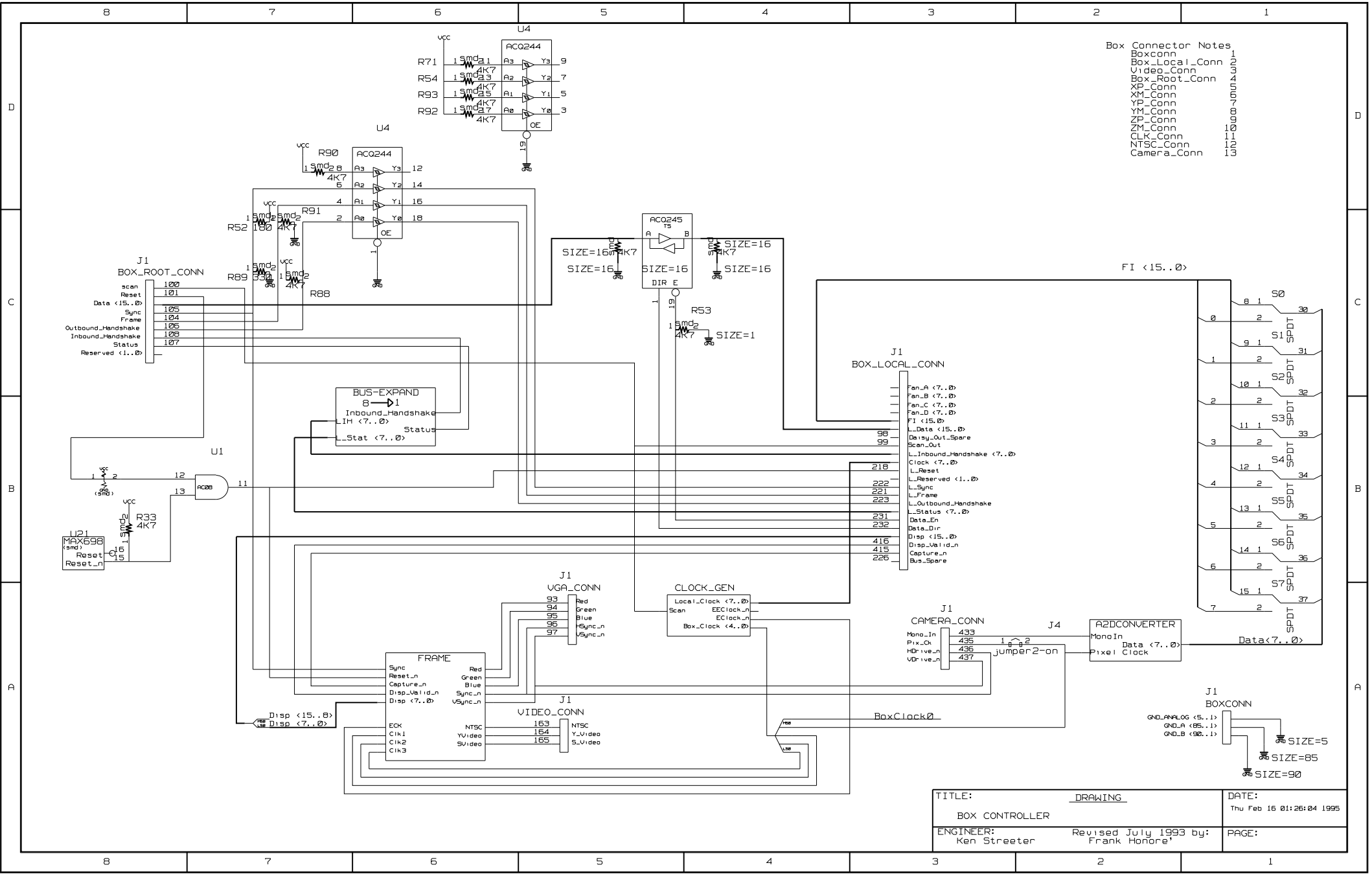
Box Connector Notes
Boxconn 1
Box_Local_Conn 2
Video_Conn 3
Box_Root_Conn 4
XP_Conn 5
XM_Conn 6
YP_Conn 7
YM_Conn 8
ZP_Conn 9
ZM_Conn 10
CLK_Conn 11
NTSC_Conn 12
Camera_Conn 13
U4
ACQ244
R71
R54
R93
R92
R90
R91
R52
R89
R88
ACQ245
SIZE=16
SIZE=1
R53
J1
BOX_ROOT_CONN
scan
Reset
Data <15..0>
Sync
Frame
Outbound_Handshake
Inbound_Handshake
Status
Reserved <1..0>
BUS-EXPAND
Inbound_Handshake
LIM <7..0>
Status
L_Stat <7..0>
U1
AC08
U21
MAX698
Reset
Reset_n
R33
J1
BOX_LOCAL_CONN
Fan_A <7..0>
Fan_B <7..0>
Fan_C <7..0>
Fan_D <7..0>
FI <15..0>
L_Data <15..0>
Daisy_Out_Spare
Scan_Out
L_Inbound_Handshake <7..0>
Clock <7..0>
L_Reset
L_Reserved <1..0>
L_Sync
L_Frame
L_Outbound_Handshake
L_Status <7..0>
Data_En
Data_Dir
Disp <15..0>
Disp_Valid_n
Capture_n
Bus_Spare
FI <15..0>
S0
S1
S2
S3
S4
S5
S6
S7
SPDT
Data<7..0>
J1
VGA_CONN
Red
Green
Blue
HSync_n
VSync_n
CLOCK_GEN
Local_Clock <7..0>
Scan
EEClock_n
EClock_n
Box_Clock <4..0>
J1
CAMERA_CONN
Mono_In
Pix_Ck
HDrive_n
VDrive_n
J4
jumper2-on
A2DCONVERTER
MonoIn
Data <7..0>
Pixel Clock
FRAME
Sync
Reset_n
Capture_n
Disp_Valid_n
Disp <7..0>
ECK
Clk1
Clk2
Clk3
Red
Green
Blue
Sync_n
VSync_n
NTSC
YVideo
SVideo
J1
VIDEO_CONN
NTSC
Y_Video
S_Video
Disp <15..8>
Disp <7..0>
BoxClock0
J1
BOXCONN
GND_ANALOG <5..1>
GND_A <85..1>
GND_B <90..1>
SIZE=5
SIZE=85
SIZE=90
TITLE:
DRAWING
BOX CONTROLLER
DATE:
Thu Feb 16 01:26:04 1995
ENGINEER:
Ken Streeter
Revised July 1993 by:
Frank Honore'
PAGE: