Project 9904-04: Malleable Architectures for Adaptive Computing
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Project Overview

• TRS
  – High Level Architecture Specification Language and Synthesis

• Malleable Architectures
  – Arch. Specific ISA
  – Malleable Caches

Progress Through December 2000

- Analysis of Memory Behavior of Speech Applications
  - Whole system simulation (realistic environment)
  - Automatic identification of address streams
  - Improvements with manual cache partitioning

- Developed Accurate Models of Cache Behavior of Multitasking Environment
Research Plan for the Next Six Months

- Apply Cache Partitioning to
  - Full Speech Processing (SLS)
  - Image Understanding
- Analyze Other Apps
- Analyze Predictability