



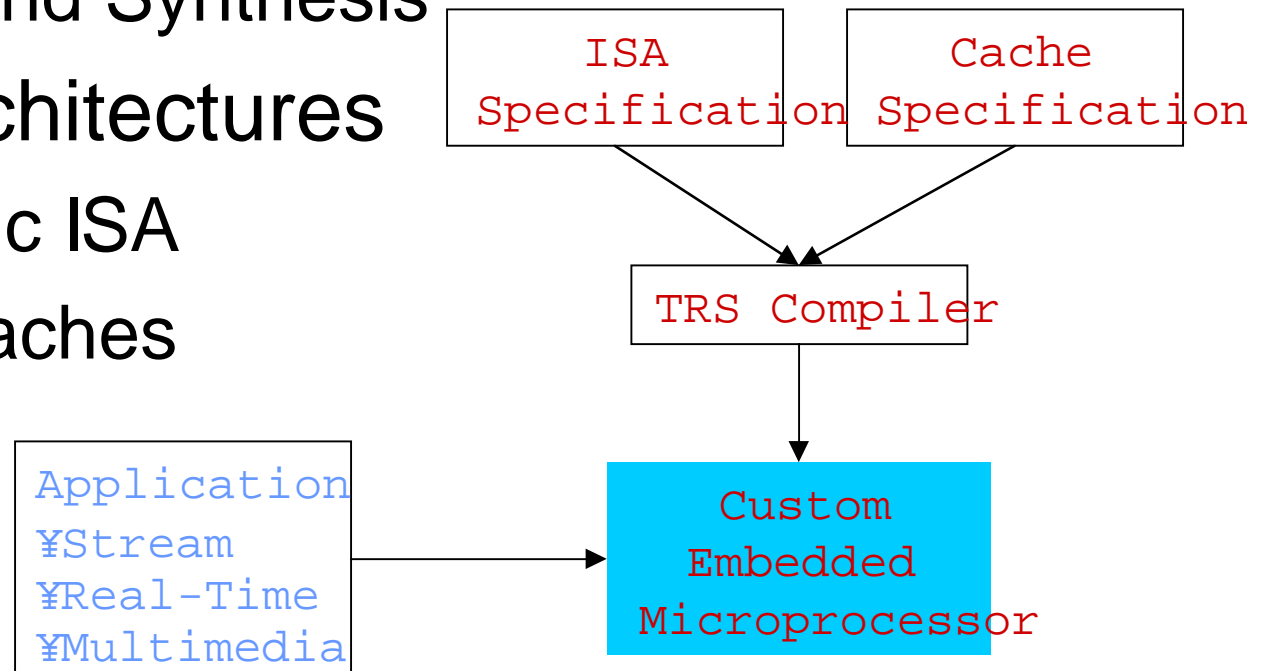
Project 9904-04: Malleable Architectures for Adaptive Computing

Arvind, Larry Rudolph and Srinivas Devadas



Project Overview

- TRS
 - High Level Architecture Specification Language and Synthesis
- Malleable Architectures
 - Arch. Specific ISA
 - Malleable Caches





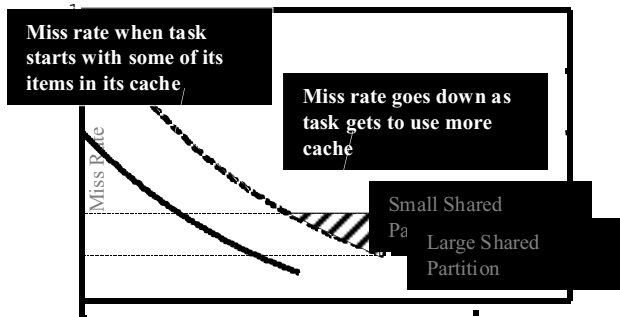
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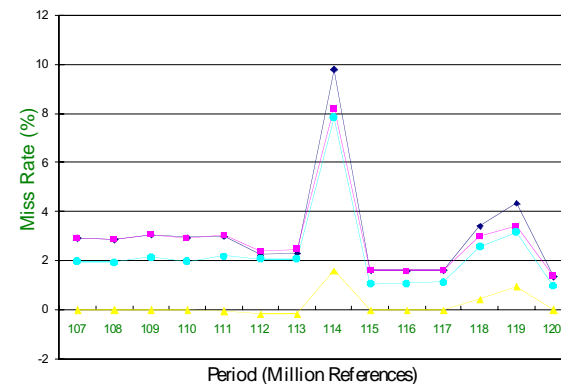


Progress Through December 2000

- Analysis of Memory Behavior of Speech Applications
 - Whole system simulation (realistic environment)
 - Automatic identification of address streams
 - Improvements with manual cache partitioning
- Developed Accurate Models of Cache Behavior of Multitasking Environment



Length of Time Quantum





Research Plan for the Next Six Months

- Apply Cache Partitioning to
 - Full Speech Processing (SLS)
 - Image Understanding
- Analyze Other Apps
- Analyze Predictability

